

a number of magnitude wavelet transform coefficient bitplanes to be truncated during the quantization, each of the magnitude wavelet transform coefficient bitplanes representing a magnitude level associated with wavelet transform coefficients, the three bits indicating that all bitplanes are to be truncated when the three bits are in predetermined logic states~~three bit values to apply to the sequence of image data, the three bit values specify a number of bitplanes to be truncated during the quantization, a value with all three bits having an identical logical value indicates that all bitplanes are to be truncated, each of the bitplanes corresponding to a one-bit-per-pixel image capable of being displayed overlaying with one or more other bitplanes, a quantization value is converted into up to seven quantizers using the three bit values, and wherein for consecutive pair of quantization values including at least one pair of 0 and 1, 2 and 3, 4 and 5, $8i+6$, and $8i+7$ for integers of i that are greater than or equal to zero, substantially the same quantizer is utilized.~~

2. (Previously Presented) The method defined in Claim 1 further comprising:
coding bitplanes specified for application of the scalar quantization.
3. (Original) The method defined in Claim 2 wherein non-specified bit planes are not coded.
4. (Previously Presented) The method defined in Claim 1 wherein all bitplanes are truncated during the quantization when each of the three bits representing the three bit values is a logical one.
5. (Original) The method defined in Claim 1 wherein the three bit values specify 0, 1, 2, 3, 4, 5, 6, or all bit planes for truncation.

6. (Original) The method defined in Claim 1 wherein specifying scalar quantization comprises specifying scalar quantization for individual frames of a motion video sequence.
7. (Original) The method defined in Claim 6 wherein the video sequence comprises a motion JPEG 2000 Standard video sequence.
8. (Original) The method defined in Claim 1 further comprising writing the three bit values to a controller to cause the controller to control compression hardware.
9. (Currently Amended) An apparatus comprising:
- means for receiving a sequence of image data to compress;
 - means for specifying scalar quantization with a power of two step size using only three bit values to represent a quantizer that is applied to the sequence of image data, the three bit values specifying a number of magnitude wavelet transform coefficient bitplanes to be truncated during the quantization, each of the magnitude wavelet transform coefficient bitplanes representing a magnitude level associated with wavelet transform coefficients, the three bits indicating that all bitplanes are to be truncated when the three bits are in predetermined logic states~~three bit values for the compressed data, the three bit values specify a number of bitplanes to be truncated during the quantization, a value with all three bits having an identical logical value indicates that all bitplanes are to be truncated, each of the bitplanes corresponding to a one-bit per pixel image capable of being displayed overlaying with one or more other bitplanes, a quantization value is converted into up to seven quantizers using the three bit values, and wherein for consecutive pair of quantization values including at least one pair of 0 and 1, 2 and 3, 4 and 5, $8i+6$, and $8i+7$ for integers of i that are greater than or equal to zero, substantially the same quantizer is utilized.~~

10. (Previously Presented) The apparatus defined in Claim 9 further comprising means for coding bitplanes specified for application of the scalar quantization.
11. (Original) The apparatus defined in Claim 9 wherein non-specified bit planes are not coded.
12. (Previously Presented) The apparatus defined in Claim 9 wherein all bitplanes are truncated during the quantization when each of the three bits representing the three bit values has a logical value of one.
13. (Original) The apparatus defined in Claim 9 wherein the three bit values specify 0, 1, 2, 3, 4, 5, 6, or all bit planes for truncation.
14. (Original) The apparatus defined in Claim 9 wherein specifying scalar quantization comprises specifying scalar quantization for individual frames of a motion video sequence.
15. (Original) The apparatus defined in Claim 14 wherein the video sequence comprises a motion JPEG 2000 Standard video sequence.
16. (Original) The apparatus defined in Claim 9 further comprising writing the three bit values to a controller to cause the controller to control compression hardware.
17. (Currently Amended) An apparatus for compressing image data comprising:
a controller to specify scalar quantization with a power of two step size using three bit values to be applied to the image data;

a compressor coupled to the controller to compress a sequence of image data to create compressed data, the compressor comprising a quantizer responsive to the scalar quantization specified by the controller to quantize the image data, the quantizer represented by only three bit values, the three bit values specifying a number of magnitude wavelet transform coefficient bitplanes to be truncated during the quantization, each of the magnitude wavelet transform coefficient bitplanes representing a magnitude level associated with wavelet transform coefficients, the three bits indicating that all bitplanes are to be truncated when the three bits are in predetermined logic states~~the three bit values specify a number of bitplanes to be truncated during the quantization, a value with all three bits having an identical logical value indicates that all bitplanes are to be truncated, each of the bitplanes corresponding to a one-bit-per-pixel image capable of being displayed overlaying with one or more other bitplanes, a quantization value is converted into up to seven quantizers using the three bit values, and wherein for consecutive pair of quantization values including at least one pair of 0 and 1, 2 and 3, 4 and 5, $8i+6$, and $8i+7$ for integers of i that are greater than or equal to zero, substantially the same quantizer is utilized.~~

18. (Original) The apparatus defined in Claim 13 wherein the compressed data is compliant with the JPEG 2000 Standard.

19. (Previously Presented) The apparatus defined in Claim 17 wherein the quantizer performs coding bitplanes specified for application of the scalar quantization.

20. (Currently Amended) An article of manufacture comprising one or more computer recordable media having executable instructions stored thereon which, when executed by a computer, cause the computer to:

receive a sequence of image data to compress;

specify scalar quantization with a power of two step size using only three bit values to represent a quantizer that is applied to the sequence of image data, the three bit values specifying a number of magnitude wavelet transform coefficient bitplanes to be truncated during the quantization, each of the magnitude wavelet transform coefficient bitplanes representing a magnitude level associated with wavelet transform coefficients, the three bits indicating that all bitplanes are to be truncated when the three bits are in predetermined logic states~~three bit values for the compressed data, the three bit values specify a number of bitplanes to be truncated during the quantization, a value with all three bits having an identical logical value indicates that all bitplanes are to be truncated, each of the bitplanes corresponding to a one-bit-per-pixel image capable of being displayed overlaying with one or more other bitplanes, a quantization value is converted into up to seven quantizers using the three bit values, and wherein for consecutive pair of quantization values including at least one pair of 0 and 1, 2 and 3, 4 and 5, $8i+6$, and $8i+7$ for integers of i that are greater than or equal to zero, substantially the same quantizer is utilized.~~

21. (Previously Presented) The method of claim 1, wherein a number of bitplanes to be truncated is greater than 7.

22. (Previously Presented) The apparatus of claim 9, wherein a number of bitplanes to be truncated is greater than 7.

23. (Previously Presented) The apparatus of claim 17, wherein a number of bitplanes to be truncated is greater than 7.

24. (Previously Presented) The article of claim 20, wherein a number of bitplanes to be truncated is greater than 7.